

# Addressing Test Challenges with Solid Contact Technology

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**Johnstech®**

# Agenda

- The Challenges and Solutions
  - Linearity, Gain and Noise in RF Amplifiers
  - Testing Devices Susceptible to Ground Inductance
  - Testing CRES-sensitive devices
- Evolution of Solid Contacts
  - New VROL Technology

# LINEARITY, GAIN AND NOISE IN RF AMPLIFIERS

# Testing RF Amplifiers

- The 1 dB compression point (P1dB) is the output power level at which the gain decreases 1 dB from its constant value.
- Once an amplifier reaches its P1dB it goes into compression and becomes a non-linear device, producing distortion, harmonics and intermodulation products.
- Accurately measuring the P1dB and gain are one of the most important specifications for power amplifiers, as it is up to this point that we consider an amplifier to operate linearly.

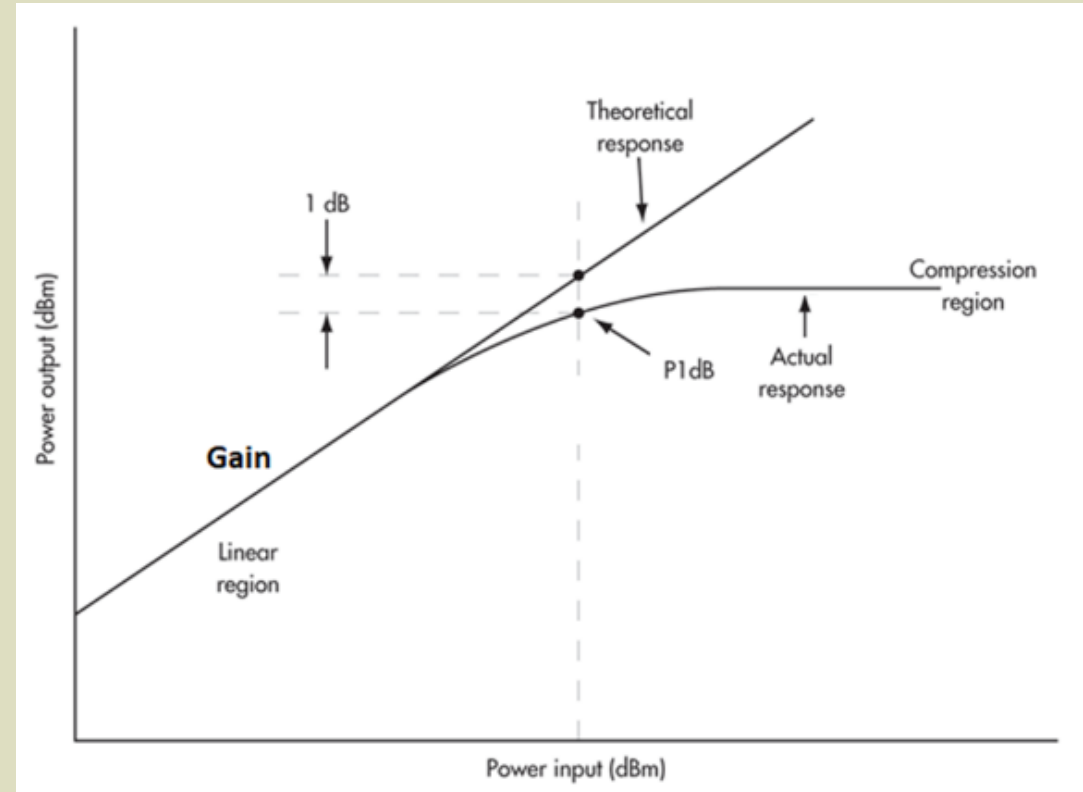


Figure 1 [1]

# Testing RF Amplifiers

- Factors that impact measurement accuracy of P1dB and gain:
  - Source/load impedance mismatch
  - Noise figure (increases with losses incurred before the amplifier input)
  - Frequency Response

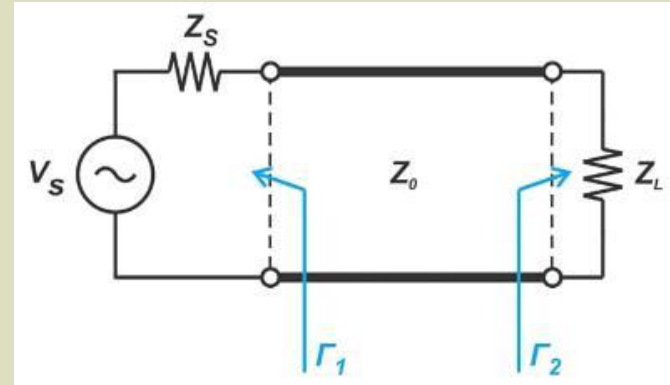


Figure 2 [2]

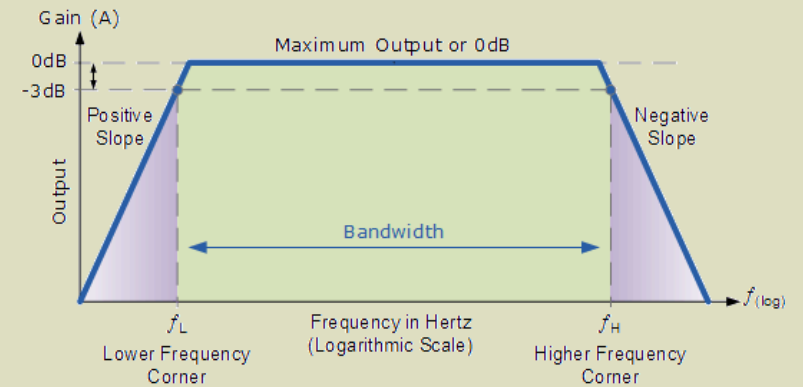
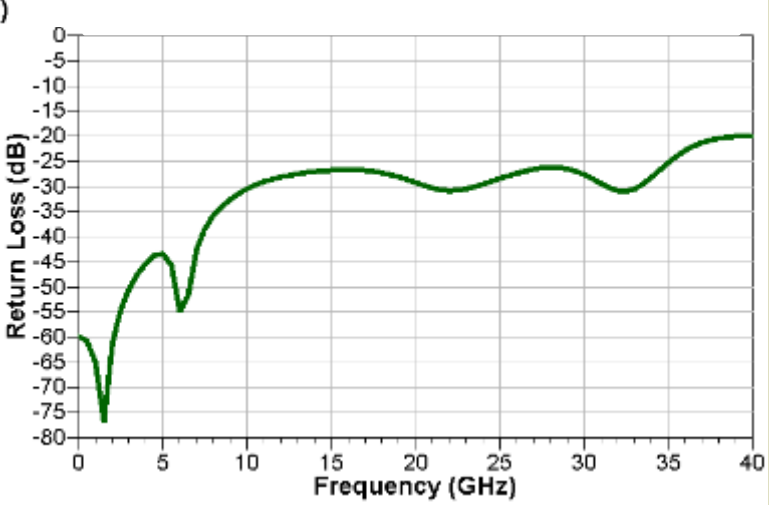
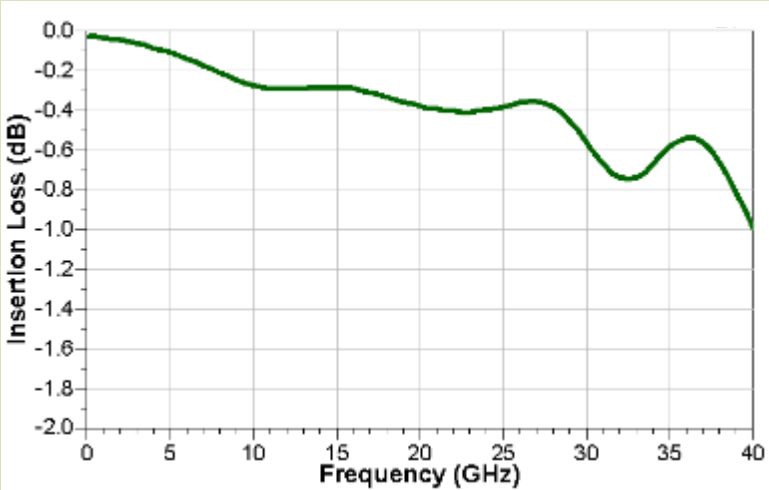


Figure 3 [3]

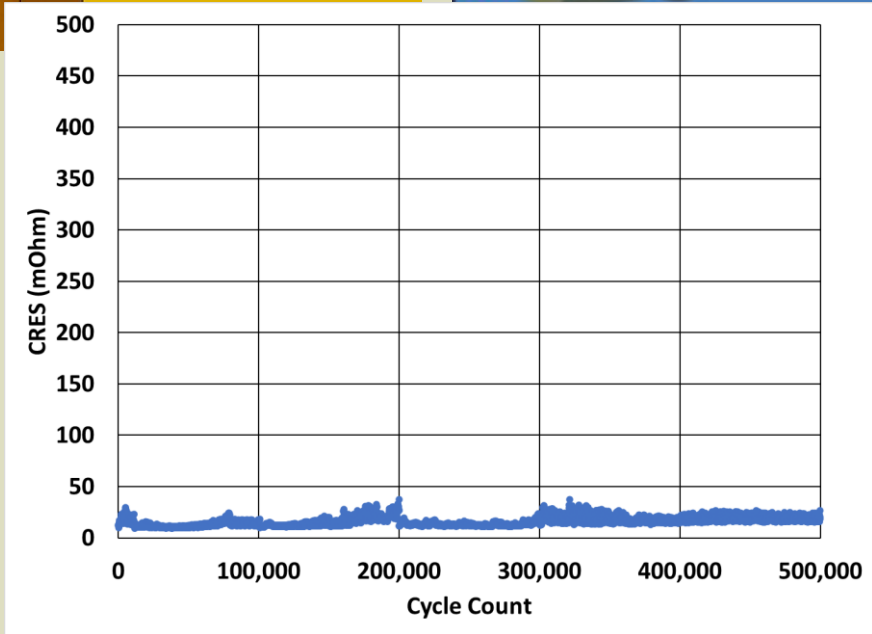
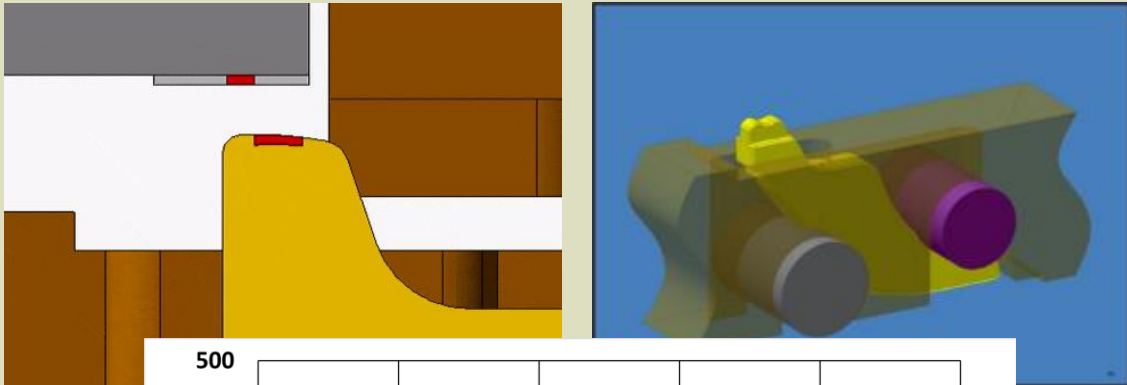
Select a contactor with a very low loss, well impedance matched and low consistent CRES, especially at higher frequencies

# Solution for RF Amplifiers – Solid Contacts

Low loss and matched impedance

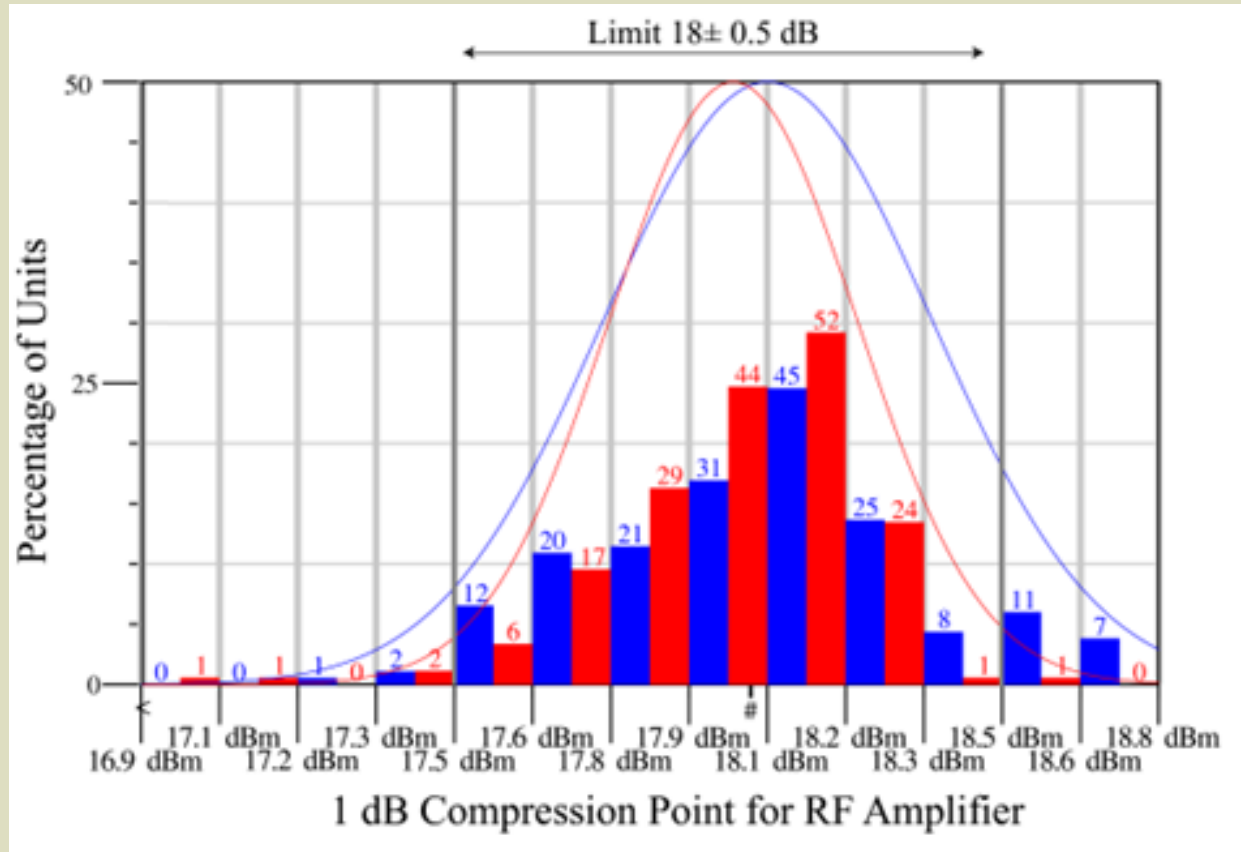


Self cleaning scrub and low consistent CRES



# Customer Application #1

## RF Amplifier 1dB Compression Point



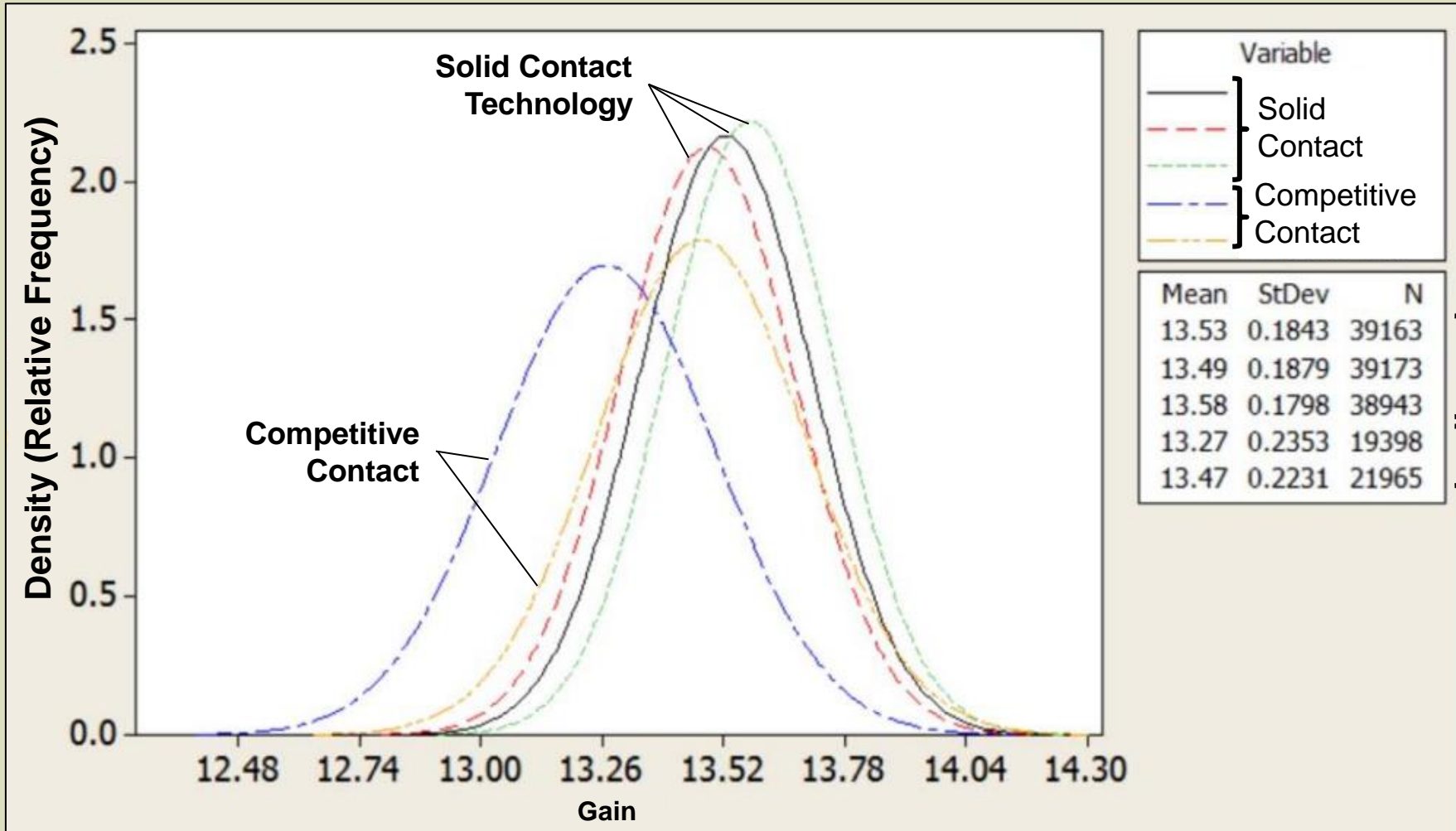
■ Solid Contact Technology

■ Competitive Contact

Yield Summary		
Contacts	Solid Contact Technology	Competitive Contact
Pass	173	162
Fail	5	21
Total	178	183
Yield	97.2%	88.5%

# Customer Application #2

## Amplifier Gain



Higher and more repeatable gain with solid-contact technology



# Customer Application #2

## Return Loss Performance

### Return Loss Performance

	Mean	Stdev	N
Solid	-26.08	0.9766	39163
Contact	-26.20	0.9862	39173
Technology	-25.91	1.152	38943
Competitive	-24.09	1.964	19398
Contact	-24.30	1.514	21965

Solid contacts provided better matched impedance and more repeatable performance.

# TESTING DEVICES SUSCEPTIBLE TO GROUND INDUCTANCE

# Effect of Inductance on Power Amplifiers

For Power Amplifiers, additional inductance leads to ground bounce, introducing voltage noise in the high inductance return path. [4]

The inductance will affect the efficiency and gain of the power amplifier.

- Efficiency decreases as output power increases
- Output power is reduced

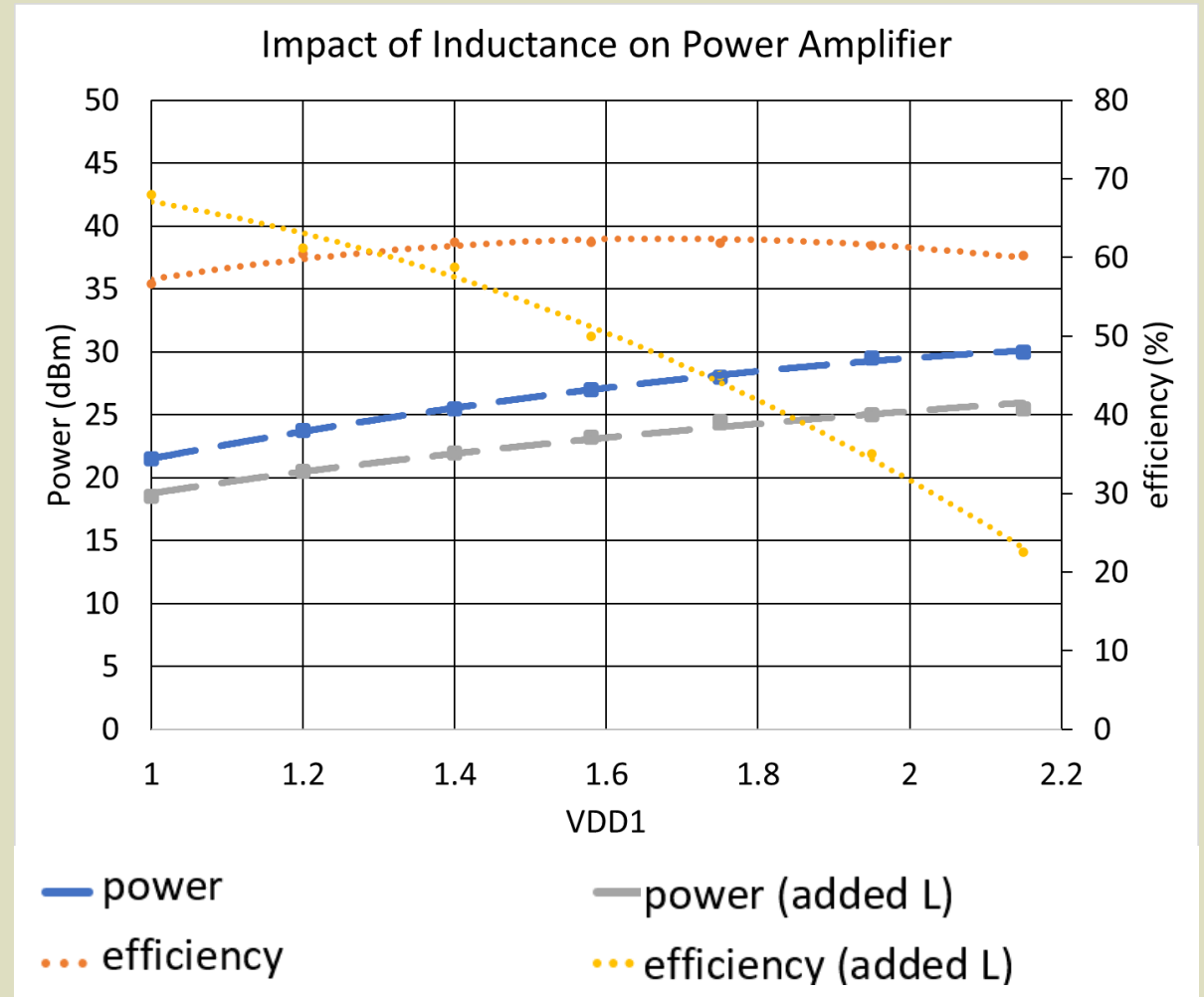
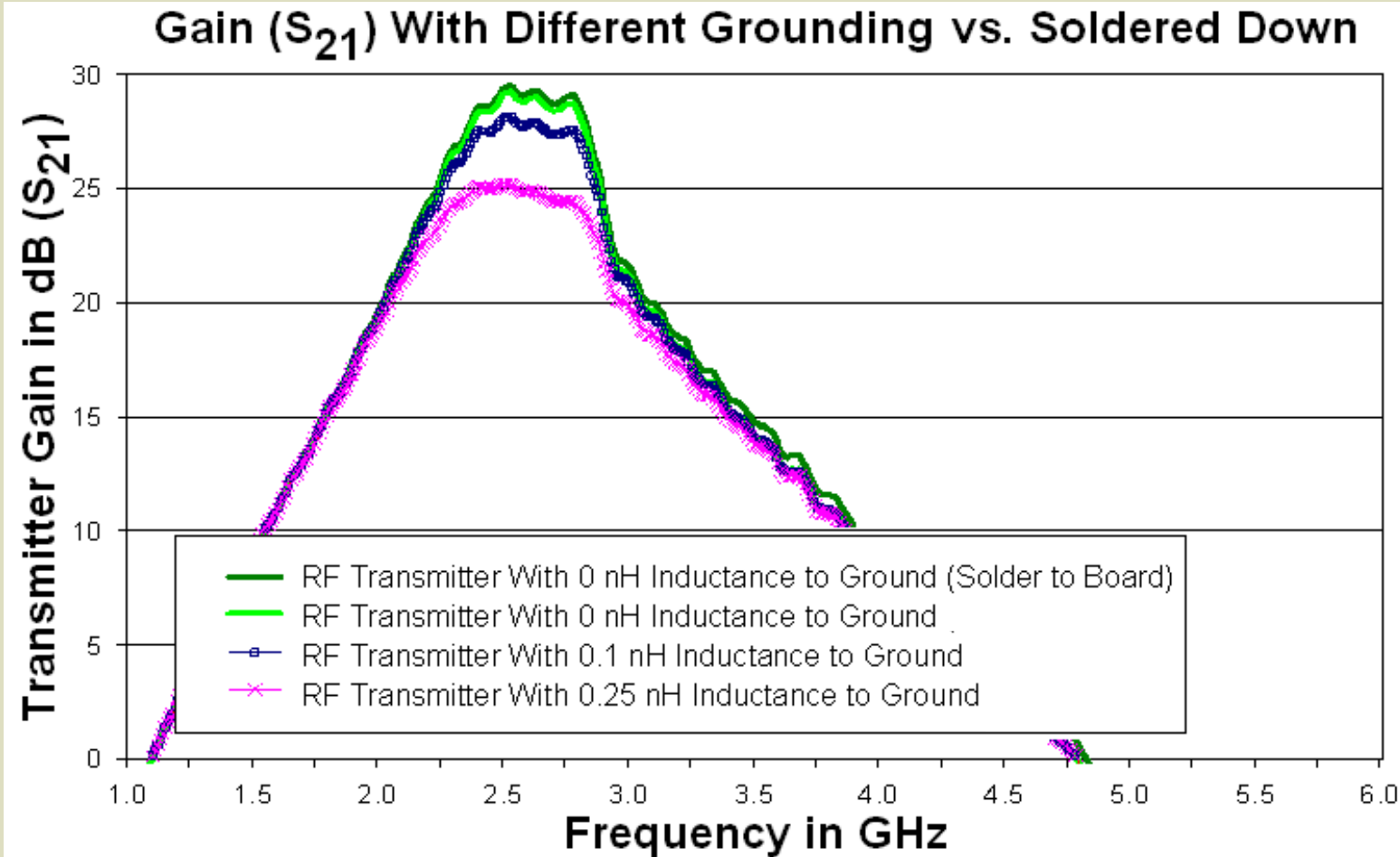


Figure 4 [5]

# Effects of High Inductance on Power Amplifiers



Amplifier gains above 20 dB more sensitive to ground inductance

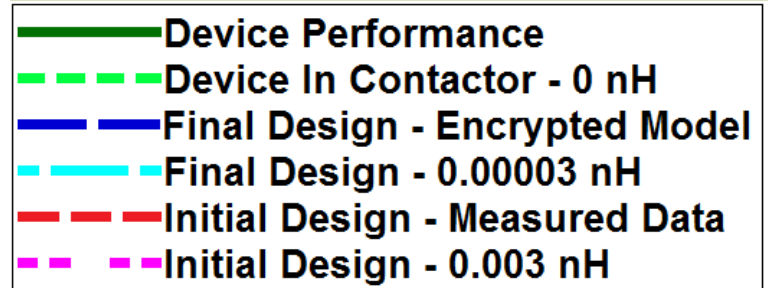
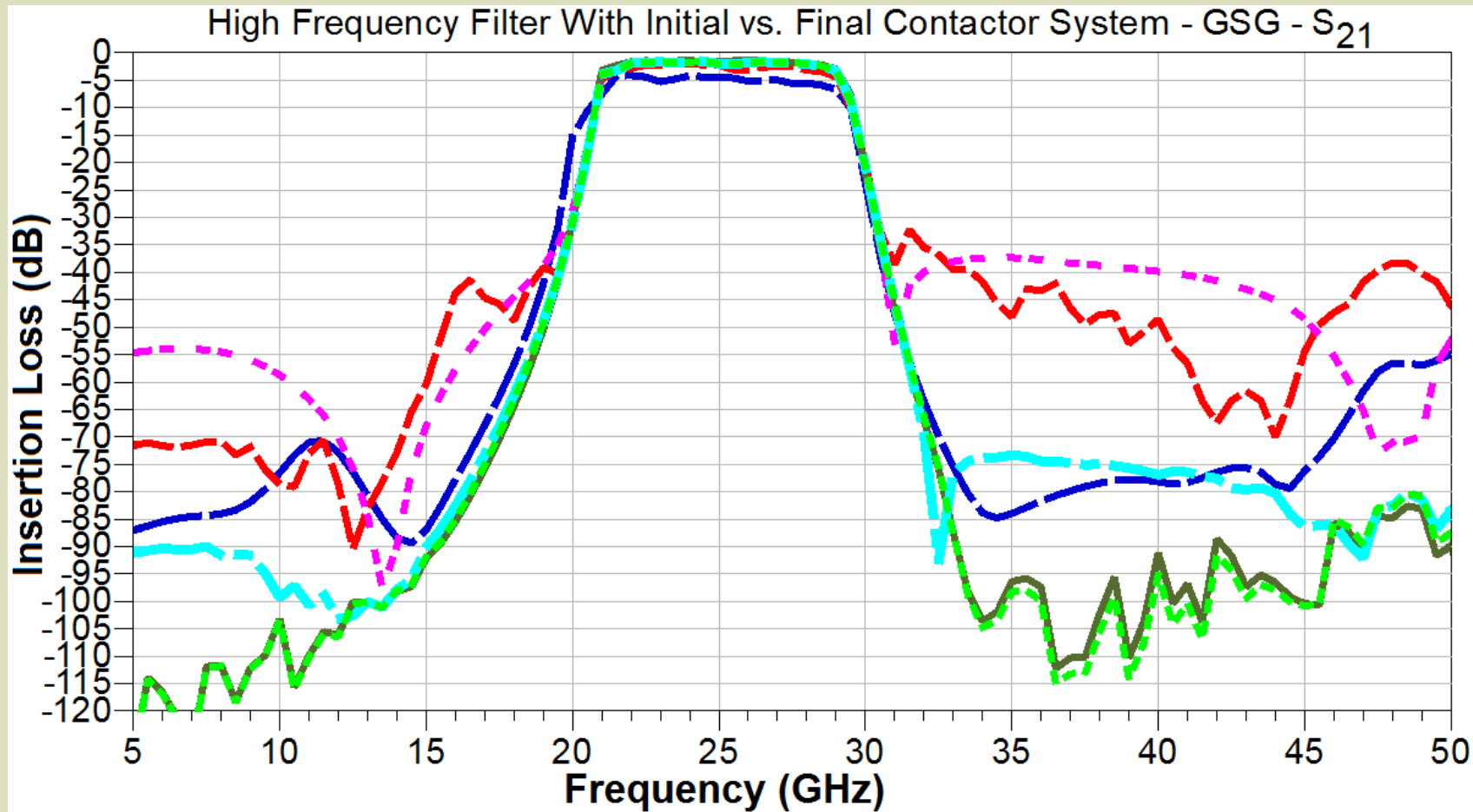
The higher the amplifier gain the more important it is to use short rigid contacts to maintain low inductance

# Devices that are Susceptible to Ground Inductance in Test System

- Power Amplifiers
- High Gain Amplifiers (Above 20dB)
- Filters Surface Acoustic Wave (SAW) and Bulk Acoustic Wave (BAW)
- High-Frequency Designs – Above 3GHz
- High-Speed Digital Designs – Above 10 GBits/sec
- High Gain Devices like RX and TX Devices (above 20dB)
- Voltage-sensitive devices – (i.e., High BIT count DACs and ADCs - Voltage per BIT small)

[6]

# Impact of Ground Inductance on Bulk Acoustic Wave Filters



The steeper a filter skirt the more important it is to use short rigid contacts to maintain low inductance.

Figure 5 [6]

# ACCURATELY TESTING RESISTANCE-SENSITIVE DEVICES

# ADC Errors – Input Resistance

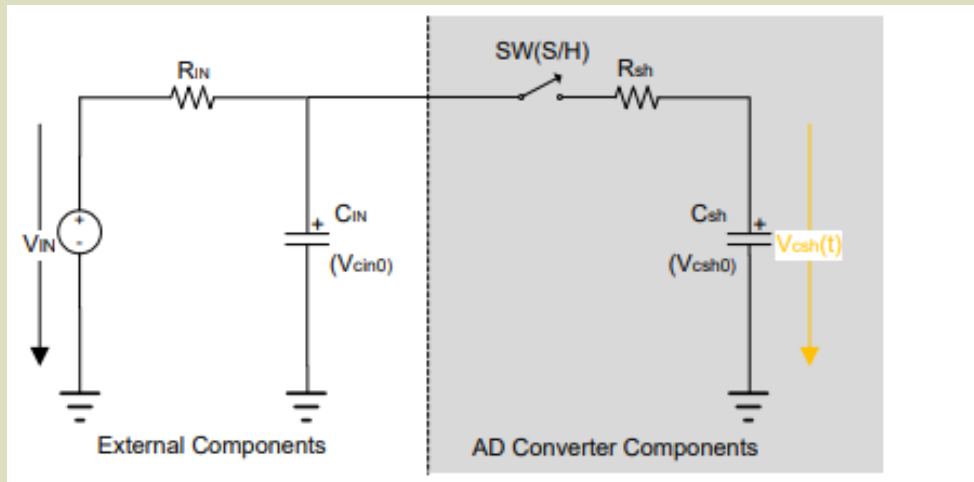


Figure 6 [7]

- Excessive analog signal source resistance can impact the settling time which introduces ADC errors
- With additional sources of resistance, the time required to fully charge the hold capacitor increases.
- Offset and Gain errors cause deviation from ideal performance due to increased source resistance.

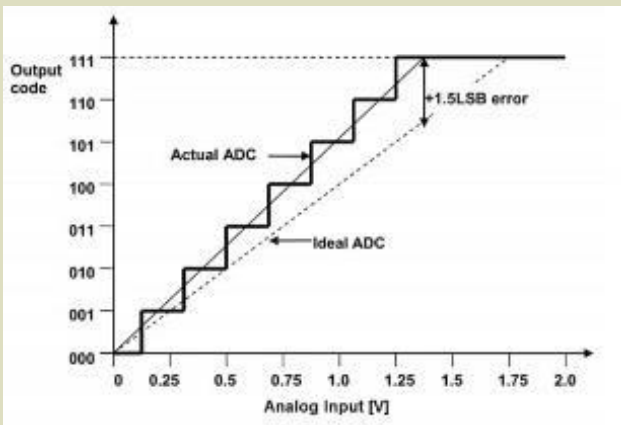


Figure 7 [8]

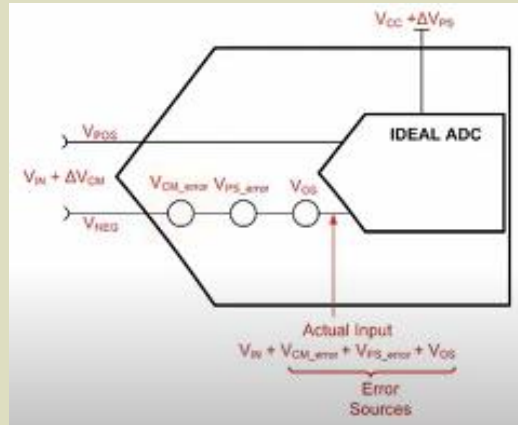


Figure 8 [9]

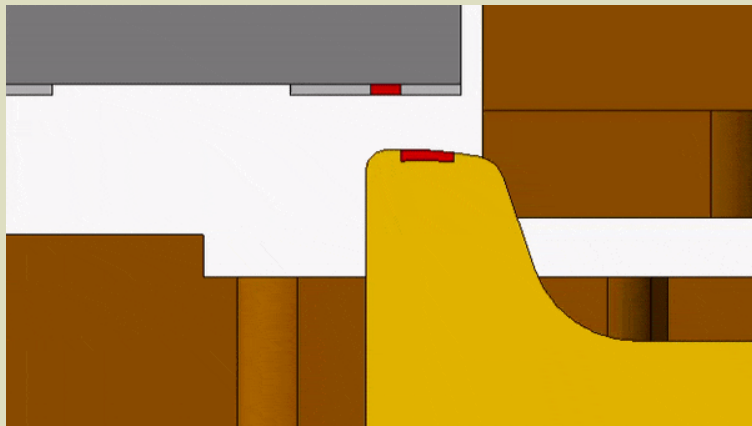


# High Resistance and Impact to Production

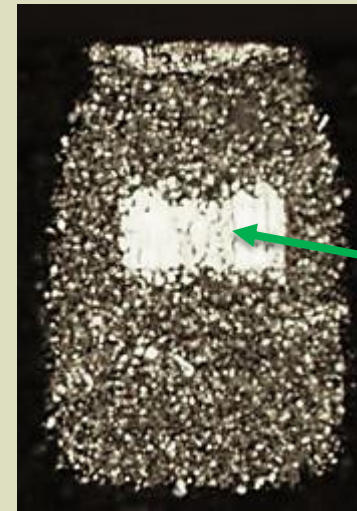
- Oxide-rich matte tin causes increased and highly variable contact resistance, resulting in lower yields.
- Matte tin from the package quickly builds up on the contact pins, which causes yields to fall, due to the increased variability of contact pin resistance values.
- While more frequent cleaning seems to counter the oxide buildup of matte tin, the increased contactor cleaning may also result in a throughput drop.

# Oxide-Penetration with ROL Self-Cleaning Scrub

- Contactor with a **self-cleaning feature** can help reduce the frequency of contactor cleaning. A self-cleaning feature will also help delay amalgamation between the gold plating and tin.

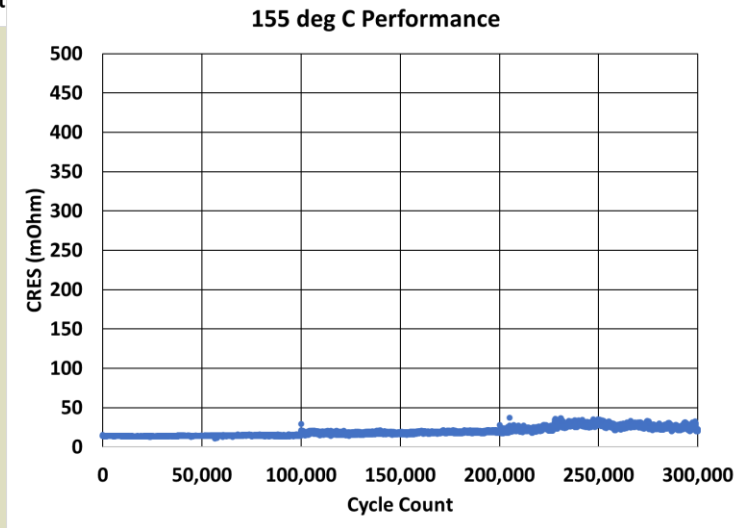
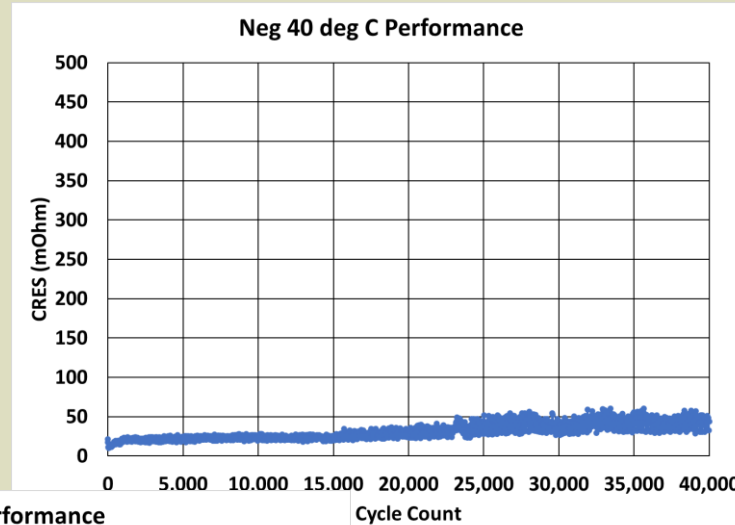
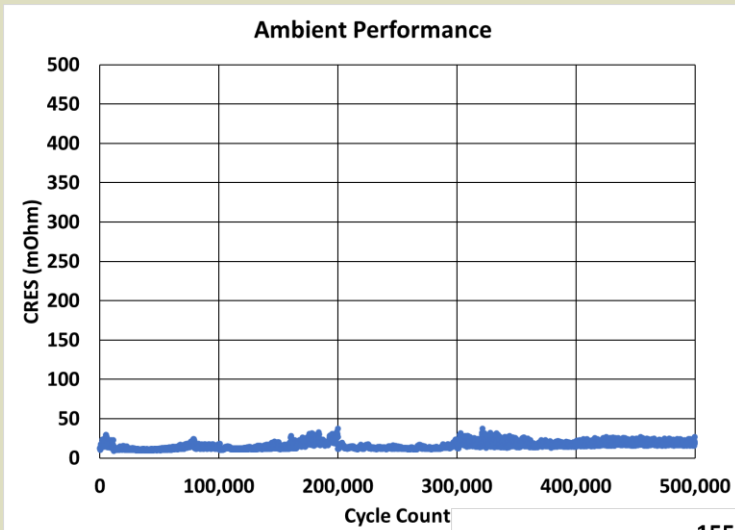


- Contacts that wipe across the surface of the I/O will remove oxides from the I/O and contact the tip on every test.

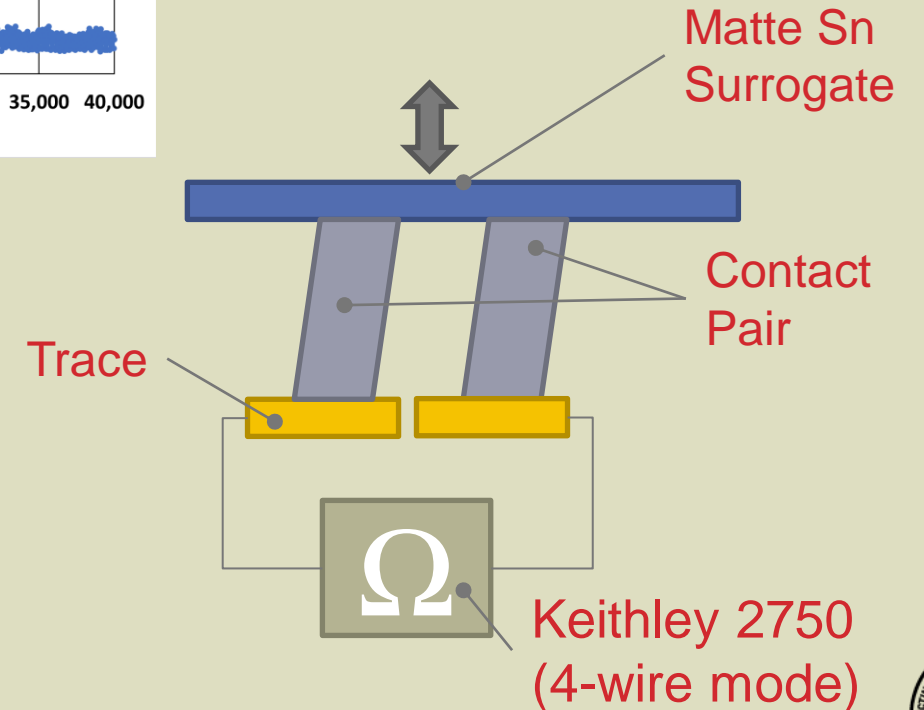


Oxide-free  
Sn for  
optimal  
CRES

# Tri-Temp CRES Performance



Self-cleaning wipe effective across temperature extremes



# EVOLVING SOLID CONTACT TECHNOLOGY

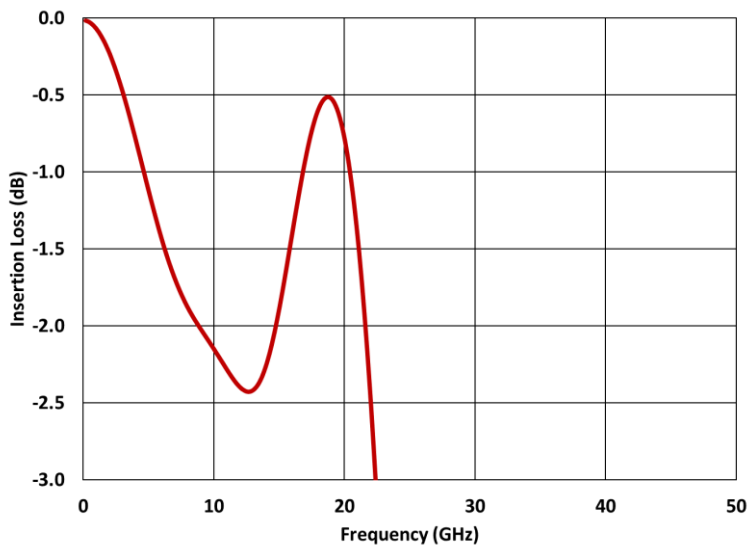


Benefits of Solid Contact Technology



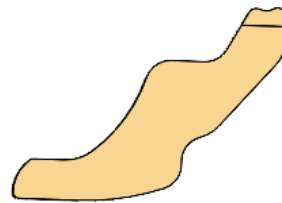
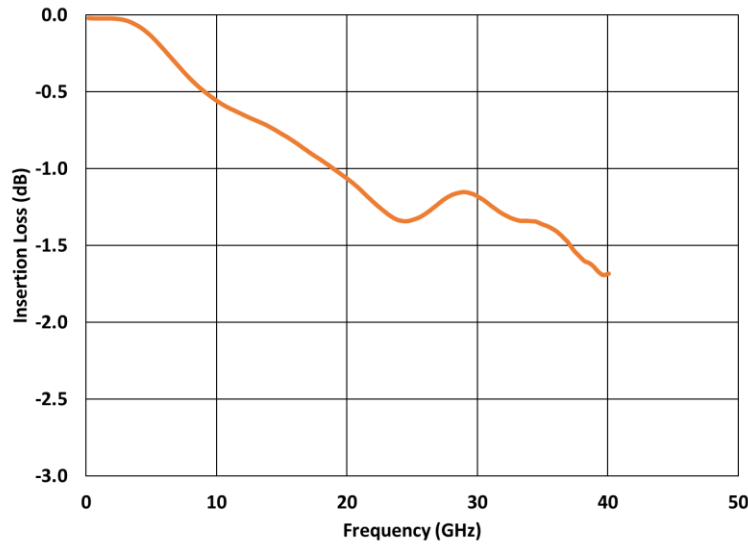
# Evolution of Solid Contacts

## S-pin



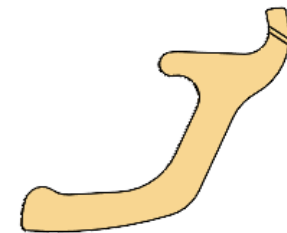
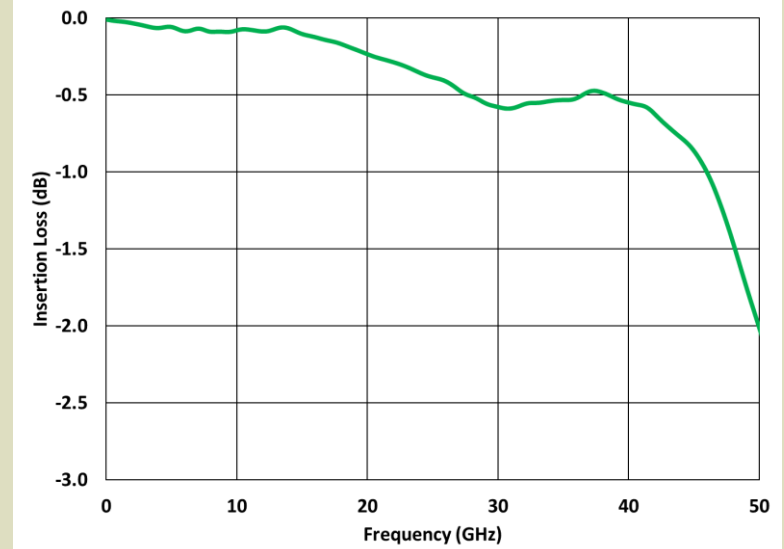
5GHz

## ROL<sup>®</sup>



20GHz

## VROL<sup>®</sup>



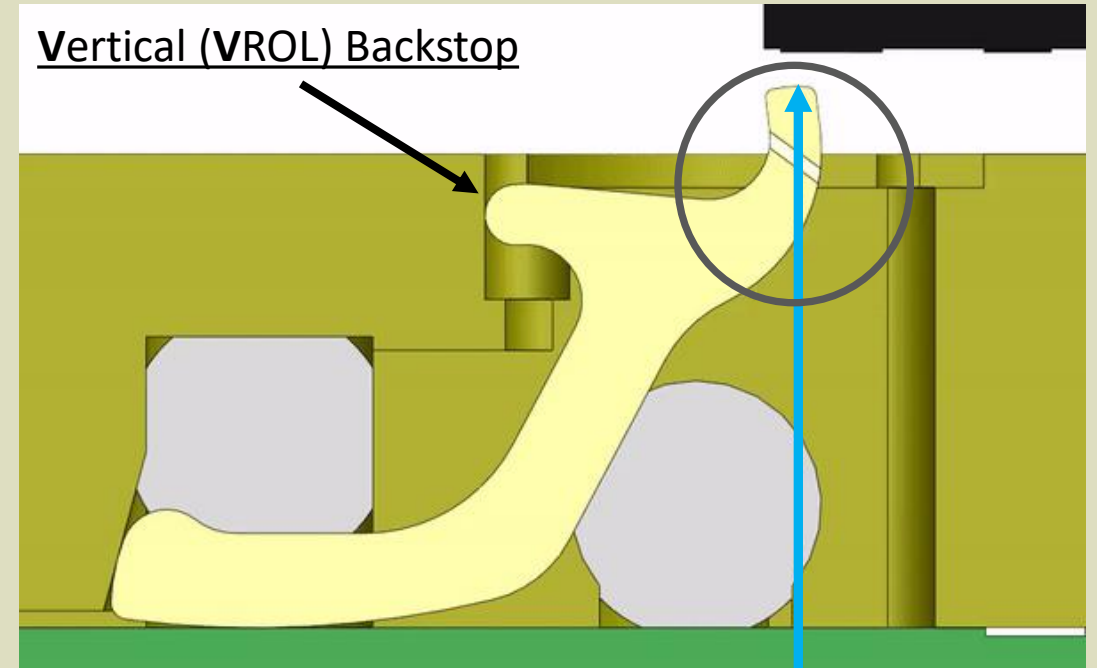
>40GHz



# New VROL® Design

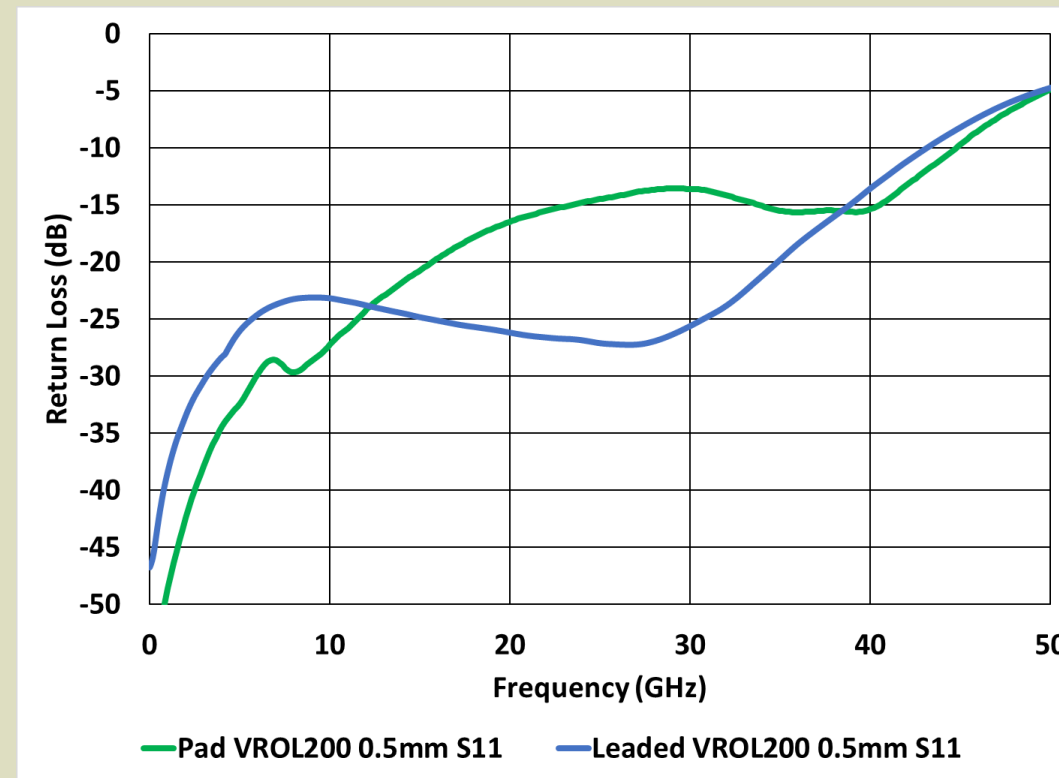
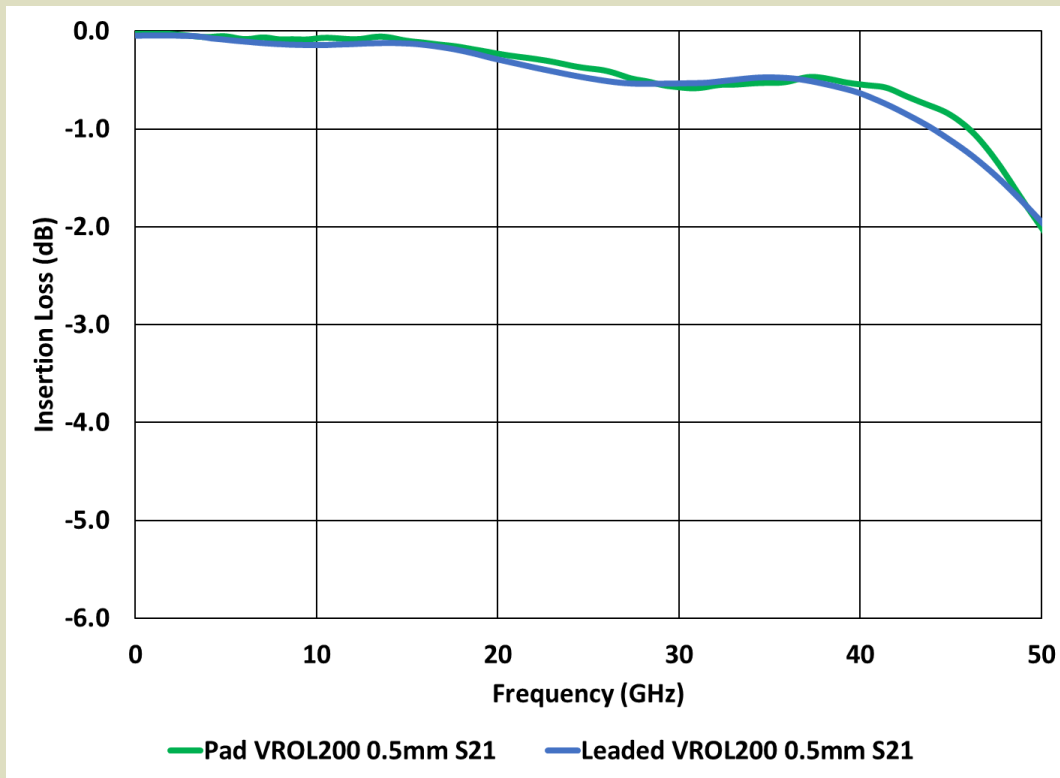
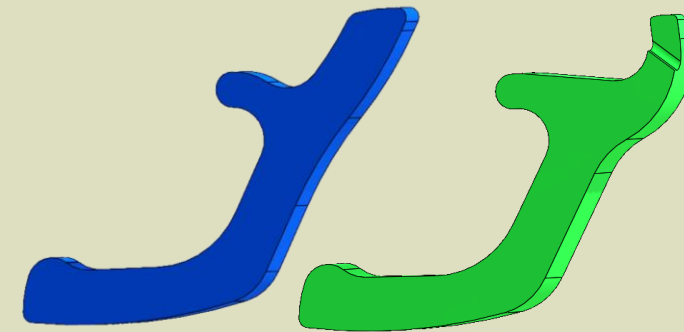
## Housing Gap Contact Alignment

The new VROL® design has a vertical-back-stop eliminates potential positional tolerances due to PCB flexing. Thus, the tip is optimized for a consistent starting point on the device pad.



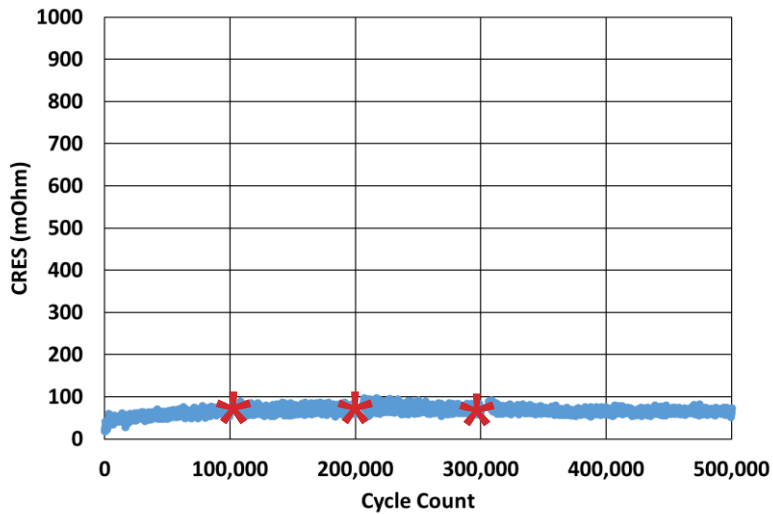


# New VROL<sup>®</sup> Design Frequency Performance

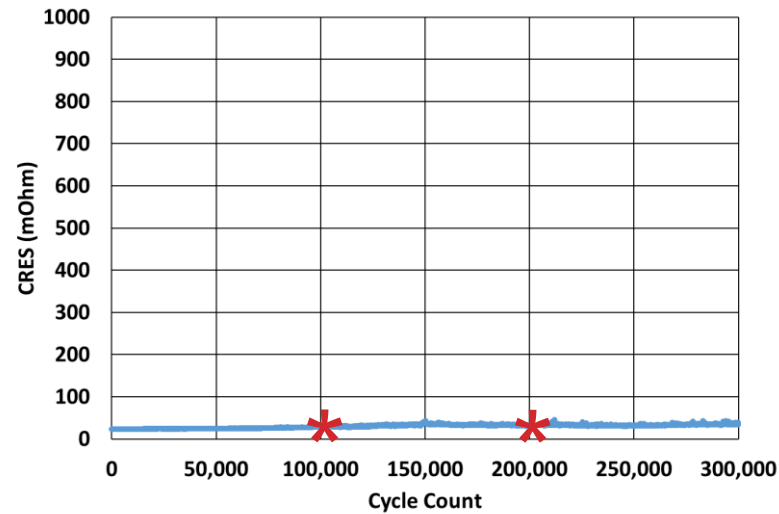


# New VROL® Design CRES

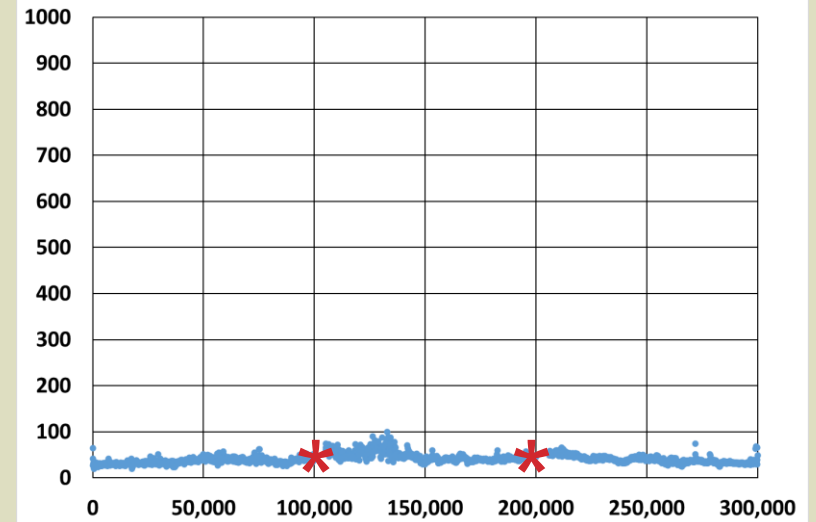
Ambient Performance



175 degC Performance



Neg 40 degC Performance

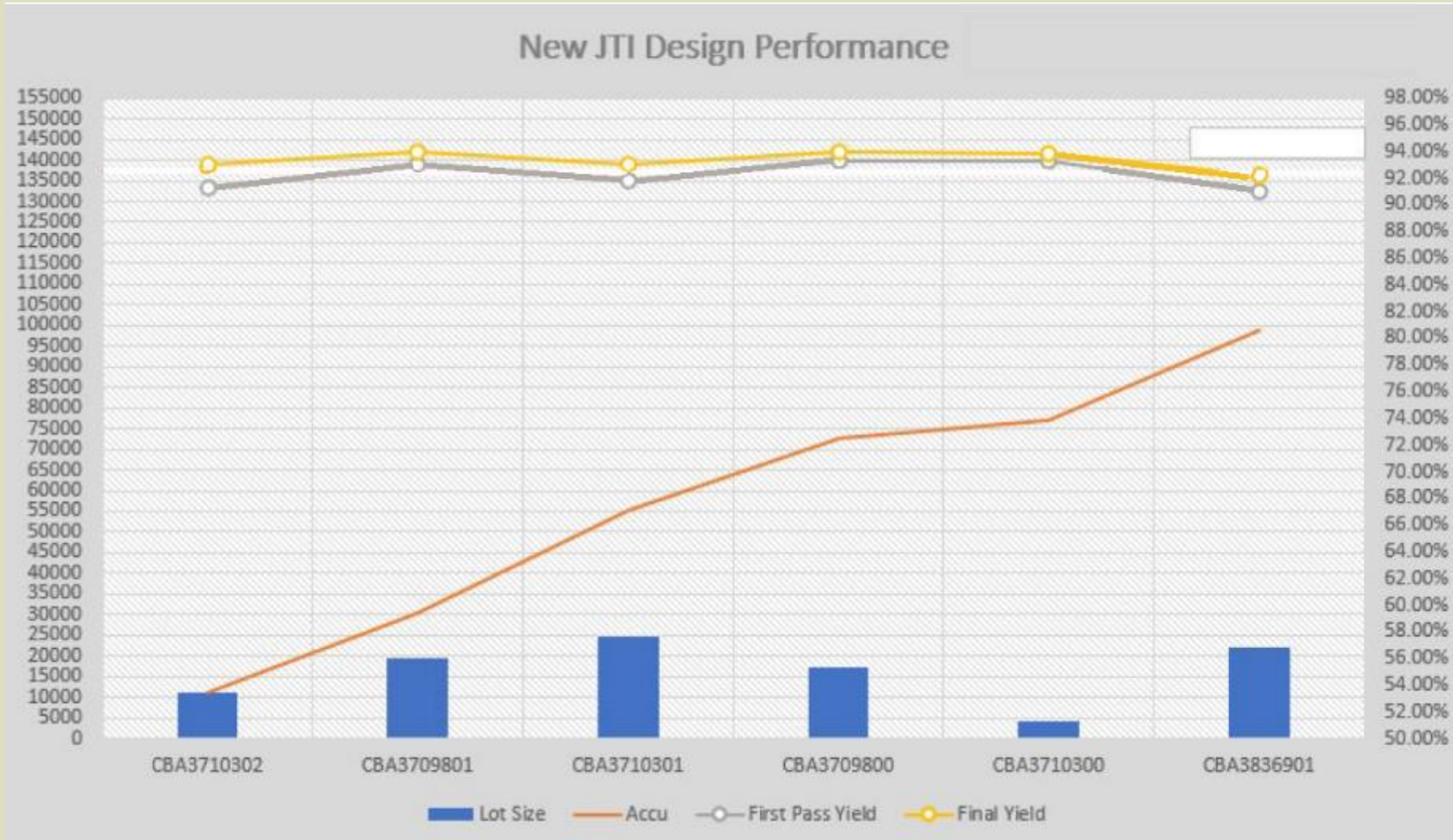


Self-cleaning wipe effective  
over temperature

\*Contactor evaluated at intervals



# VROL Customer Data



VROL Solid Contact Technology provided customer with Yield Gap performance < 2%

# Conclusions

- Single-piece solid contact construction offers minimum contact resistance
- Self-cleaning scrub maintains low contact resistance in production test
- Reduced contact height for lower ground inductance for filters, amplifiers and other sensitive to ground devices
- Rigid, one-piece contact provides RF, Digital and Cres repeatability

# References

- [1] Rajiv, R. (2022). *Ultimate guide to choosing an RF power amplifier* [Photograph]. <https://www.rfpage.com/ultimate-guide-to-choosing-an-rf-power-amplifier/>
- [2] Arar, S. (2024). *Mismatch Loss Effect on RF Power Measurement and Gain of Cascaded Amplifiers* [Photograph]. <https://www.allaboutcircuits.com/technical-articles/mismatch-loss-effect-on-radio-frequency-power-measurement-and-gain-of-cascaded-amplifiers/>
- [3] <https://www.electronics-tutorials.ws/amplifier/frequency-response.html>
- [4] Teledynelecroy, C. (n.d.). *The Causes of Ground Bounce and How To Avoid It*. Element14. <https://community.element14.com/members-area/personalblogs/b/blog/posts/the-causes-of-ground-bounce-and-how-to-avoid-it#:~:text=It%27s%20the%20voltage%20noise%20created%20by%20one%20signal,it%20guarantees%20that%20we%20will%20have%20ground%20bounce.>
- [5] Hella, M.M., & Ismail, M. (2001). *RF CMOS Power Amplifiers: Theory, Design and Implementation*.
- [6] Sherry, J. (2023, March 7). *Using Encrypted Models in HFSS to Determine System Performance*. TestConX. [https://www.testconx.org/premium/wp-content/uploads/2023/TestConX2023s7p1Sherry\\_1134.pdf](https://www.testconx.org/premium/wp-content/uploads/2023/TestConX2023s7p1Sherry_1134.pdf)
- [7] Texas Instruments (2013, May 1). *ADC Performance Parameters - Convert the Units Correctly!* www.ti.com. <https://www.ti.com/lit/an/slaa587/slaa587.pdf>
- [8] NXP (n.d.). *How to Increase the Analog-to-Digital Converter Accuracy in an Application*. www.nxp.com. <https://www.nxp.com/docs/en/application-note/AN5250.pdf>
- [9] Analog Devices (2003, March 25). *Analysis of ADC System Distortion Caused by Source Resistance*. www.Analog.com. <https://www.analog.com/en/technical-articles/analysis-of-adc-system-distortion-caused-by-source-resistance.html>